

jc713 U.S. PTO
12/14/99

12-16-99

jc530 U.S. PTO
09/461072
12/14/99

NOVAKOV ♦ DAVIS

A PROFESSIONAL CORPORATION

2000 ST PAUL PLACE
At The Arts District
750 NORTH ST. PAUL
DALLAS, TEXAS 75201-3286

(214) 922-9221

Telecopier
(214) 969-7557

ATTORNEYS AND
COUNSELORS

December 13, 1999

Commissioner of Patents
and Trademarks
Box Patent Application
Washington, D.C. 20231

EXPRESS MAIL RECEIPT No. EJ033593774US

Re: U.S. Patent Application for "DATA SYNCHRONIZATION SYSTEM AND
METHOD OF OPERATION"
Our File: SAMS001-00077

Dear Sir:

Enclosed please find the following documents for filing:

Express Mail Certificate of Mailing;
Patent Application (29 Pages including cover sheet);
Informal Drawings (3 sheets);
Declaration and Power of Attorney;
Assignment and Recordation Form Cover Sheet;
a check in the sum of \$818.00 for the filing fee and Assignment Recordal Fee; and
a postcard receipt.

FOR:	NO. FILED	NO. EXTRA	RATE	FEE
BASIC FEE		-0-	\$760.00	\$760.00
TOTAL CLAIMS	21 - 20=	1	\$ 18.00	\$ 18.00
INDEP. CLAIMS	3 - 3=	-0-	\$ 78.00	\$ 0.00
MULTIPLE DEPENDENT CLAIM PRESENTED	-0-	-0-	\$260.00	\$ 0.00

TOTAL \$778.00

Commissioner of Patents

and Trademarks

December 13, 1999

Page 2

If any problems arise in the filing of the enclosed documents, please contact William A. Munck at (214) 922-9221. The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Novakov Davis Deposit Account No. 50-0208.

Very truly yours,

NOVAKOV ♦ DAVIS, P.C.



William A. Munck

Registration No. 39,308

WAM/lmz

Enclosures

DATA SYNCHRONIZATION SYSTEM AND METHOD OF OPERATION

Inventor(s) :

Sandro Gregorat
17512 Spyglass Circle
Dallas
Collin County
Texas 75287
United States Citizen

Assignee:

SAMSUNG ELECTRONICS Co., LTD.
416, Maetan-dong, Paldal-gu
Suwon-city, Kyungki-do
Republic of Korea

<u>CERTIFICATE OF EXPRESS MAIL</u>	
I hereby certify that this correspondence, including the attachments listed, is being mailed in an envelope addressed to Commissioner of Patents and Trademarks, Washington, DC 20231, using the Express Mail Post Office to Addressee service of the United States Postal Service on the date shown below.	
<u>LAURA ZAVALA</u> Printed Name of Person Mailing <u>LAURA ZAVALA</u> Signature of Person Mailing	<u>EJD335937HUS</u> Express Mail Receipt No. <u>12/14/99</u> Date

William A. Munck
John T. Mockler
Novakov ♦ Davis, P.C.
750 North Saint Paul Street
Suite 2000
Dallas, Texas 75201-3286
(214) 922-9221

DATA SYNCHRONIZATION SYSTEM AND METHOD OF OPERATION**TECHNICAL FIELD OF THE INVENTION**

5 The present invention is directed, in general, to redundant data processing systems, more specifically, to a system for achieving and maintaining synchronization between the data in a source data file and a copy data file.

BACKGROUND OF THE INVENTION

10 Information systems have evolved from centralized mainframe computer systems supporting a large number of users to distributed computer systems based on local area network (LAN) architectures. 15 As the cost-to-processing-power ratios for computer workstations, network servers, telephone switching equipment, wireless communication devices, and the like, have dropped precipitously, wireline and wireless LAN systems have proved to be highly cost effective. As a result, the number of LANs and LAN-based 20 applications has exploded.

 To enhance the reliability of modern information systems (IS), it is common practice to build IS components as redundant devices.

For example, network switches, routers, wireless base stations, and the like, may contain a primary (also called "master" or "active") processing system that ordinarily performs the data processing functions of the device and a secondary (also called "slave" or 5 "standby") processing system that takes over from the primary processing system upon a failure or upon the occurrence of certain selected events. To properly transfer control from the primary processing system to the secondary processing system, the data file used by the primary processing system (called "source" or "original" data file) must constantly be copied to the data file used by the secondary processing system (called "copy" or "image" data file) after recovery from the failure.

10 The prior art redundant processing systems typically accomplish this by means of a first bulk copy run that copies everything in the source data file to the copy data file. Then, a 15 second bulk copy run is performed that copies to the copy data file only that data in the source data file that changed during the first copy run. This procedure is repeated until a single copy pass ends without any data changing. Thereafter, a realtime update 20 process is used to copy each change in the source data file to the copy data file as each change occurs in the source data file. However, the duration and efficiency of the above-described

procedure is greatly dependent on the rate at which data changes in the source data file. If data changes frequently, it is difficult to determine when the bulk copy will be completed so that the real time update process can take over.

5 There is therefore a need in the art for an improved redundancy architecture for use in a communication network. More particularly, there is a need in the art for an improved data synchronization system that synchronizes the data in a source data file and the data in a copy data file in a rapid and deterministic manner.

SUMMARY OF THE INVENTION

To address the above-discussed deficiencies of the prior art, it is a primary object of the present invention to provide a data synchronization apparatus for maintaining synchronization between a source data file and a copy data file. In an advantageous embodiment of the present invention, the data synchronization apparatus comprises: 1) a bulk copy controller capable of copying a plurality of data records from the source data file to the copy data file; and 2) an update controller capable of detecting a change in a data record previously copied by the bulk copy controller from the source data file to the copy data file and copying the changed data record from the source data file to the copy data file.

According to one embodiment of the present invention, the update controller and the bulk copy controller operate substantially concurrently, yet mutually exclusively to guarantee data consistency.

According to another embodiment of the present invention, the source data file comprises at least one data table comprising a plurality of data records and a synchronization descriptor associated with the at least one data table.

According to still another embodiment of the present invention, the bulk copy controller sequentially copies the plurality of data records in the at least one data table in the source data file to the copy data file and sets the synchronization descriptor to an index value of a most recently copied one of the plurality of data records.

According to yet another embodiment of the present invention, the update controller detects changes in the plurality of data records in the at least one data table in the source data file by monitoring selected ones of the plurality of data records in the at least one data table in the source data file having an index value less than the index value in the synchronization descriptor.

According to a further embodiment of the present invention, the update controller detects the changes in the plurality of data records in the at least one data table in the source data file by monitoring data write operations in the plurality of data records in the at least one data table in the source data file.

According to a yet further embodiment of the present invention, the update controller is capable of detecting that the copy data file is off line and has lost synchronization with the source data file and is capable of suspending the update process.

According to a still further embodiment of the present

invention, the update controller is capable of determining that the copy data file is on line and is capable of activating the bulk copy controller by setting at least one synchronization descriptor in the source data file to a zero value

5 The foregoing has outlined rather broadly the features and technical advantages of the present invention so that those skilled in the art may better understand the detailed description of the invention that follows. Additional features and advantages of the invention will be described hereinafter that form the subject of the claims of the invention. Those skilled in the art should appreciate that they may readily use the conception and the specific embodiment disclosed as a basis for modifying or designing other structures for carrying out the same purposes of the present invention. Those skilled in the art should also realize that such equivalent constructions do not depart from the spirit and scope of 10 the invention in its broadest form.

15

Before undertaking the DETAILED DESCRIPTION OF THE INVENTION, it may be advantageous to set forth definitions of certain words and phrases used throughout this patent document: the terms 20 "include" and "comprise," as well as derivatives thereof, mean inclusion without limitation; the term "or," is inclusive, meaning and/or; the phrases "associated with" and "associated therewith,"

as well as derivatives thereof, may mean to include, be included within, interconnect with, contain, be contained within, connect to or with, couple to or with, be communicable with, cooperate with, interleave, juxtapose, be proximate to, be bound to or with, have, 5 have a property of, or the like; and the term "controller" means any device, system or part thereof that controls at least one operation, such a device may be implemented in hardware, firmware or software, or some combination of at least two of the same. It should be noted that the functionality associated with any 10 particular controller may be centralized or distributed, whether locally or remotely. Definitions for certain words and phrases are provided throughout this patent document, those of ordinary skill in the art should understand that in many, if not most instances, such definitions apply to prior, as well as future uses of such 15 defined words and phrases.

BRIEF DESCRIPTION OF THE DRAWINGS

For a more complete understanding of the present invention, and the advantages thereof, reference is now made to the following 5 descriptions taken in conjunction with the accompanying drawings, wherein like numbers designate like objects, and in which:

FIGURE 1 illustrates an exemplary switching network containing redundant switches in accordance with the principles of the present invention;

FIGURE 2 illustrates a redundant data processing architecture 10 that may be implemented in the exemplary switch and/or the exemplary base station shown in FIGURE 1 according to one embodiment of the present invention; and

FIGURE 3 is a flow diagram illustrating the operation of the 15 bulk copy controller and the update controller in the exemplary switch and/or the exemplary base station in FIGURE 1 according to one embodiment of the present invention.

DETAILED DESCRIPTION OF THE INVENTION

FIGURES 1 through 3, discussed below, and the various embodiments used to describe the principles of the present invention in this patent document are by way of illustration only and should not be construed in any way to limit the scope of the invention. Those skilled in the art will understand that the principles of the present invention may be implemented in any suitably arranged redundant data processing system.

Although the description that follows is based upon data synchronization for switching systems and base stations with redundant processing systems, memory, and data files, those skilled in the art will also understand that the present invention may be implemented in any suitably arranged system which requires synchronization between redundant copies of one or more data files.

FIGURE 1 illustrates exemplary communication network 100 containing switches 111-114 in accordance with the principles of the present invention. Communication network 100 comprises a subnetwork 105, indicated by a dotted line, comprising switches 111-114, that interconnects end-user devices 131-134 with each other and with other switches (not shown) and other end-user devices (not shown) associated with communication network 100.

Switches 111-114 are interconnected by data links 121-126. Subnetwork 105 is intended to be a representative portion of communication network 100, which may contain many other switches similar to switches 111-114. Communication network 100 may also 5 comprise wireless equipment, such as base station 160, that enable communication network 100 to communicate with wireless devices, such as cellular telephones and/or computers equipped with cellular modems.

To enhance the reliability of communication network 100, at 10 least some of the components therein, such as switches 111-114 and base station 160, are redundant systems that include a primary (or master) processing system and a secondary (or slave) processing system. The primary processing system may switch over to the secondary processing system upon the occurrence of a failure or 15 upon a system command. In order to properly perform the switch over, however, the backup data in the secondary processing system (called "copy data") must be identical to (or "synchronized" with) the original data (called "source data") in the primary processing system. If the secondary processing system is taken 20 offline for even a short period of time, the data in the source data file and the data in the copy data file quickly lose any resemblance to one another.

This requires that a bulk copy must be performed for all the source data in the primary processing system. Also, if either the primary processing system or the secondary processing system is replaced after a failure occurs, the replacement system must be synchronized as rapidly as possible with the data in the other processing system by means of a bulk copy process. The present invention provides a unique, deterministic method for synchronizing the data in a source data file and the data in a copy data file.

FIGURE 2 illustrates a redundant data processing architecture that may be implemented in exemplary switch 111 and/or exemplary base station 160 according to one embodiment of the present invention. Exemplary switch 111 (or base station 160) comprises memory 200, memory 250, bulk copy controller 290, and update controller 295. Memory 200 stores the source (or original) data file used by the primary processing system in switch 111 (or base station 160). Memory 250 stores the copy (or image) data file used by the secondary processing system in switch 111 (or base station 160).

Within memory 200, the source data file comprises exemplary data tables 210, 220, and 230, arbitrarily labeled "Table 1," "Table 2," and "Table 3," respectively, in FIGURE 1. Memory 200 also comprises synchronization descriptor 215 (arbitrarily labeled

"SD1"), which is associated with data table 210, synchronization descriptor 225 (arbitrarily labeled "SD2"), which is associated with data table 220, and synchronization descriptor 235 (arbitrarily labeled "SD3"), which is associated with data table 230. Each synchronization descriptor is a counter register that provides the synchronization status of its associated data table, where the status is determined by bulk copy controller 290 and update controller 295. In one embodiment, each synchronization descriptor is set to zero to indicate that the data table associated with the synchronization descriptor needs to be copied to memory 250. A synchronization descriptor that is set to a non-zero integer value is an indication that data records with a label equal to or less than the non-zero value must be examined to detect a change (e.g., a data write operation) in the data record. In a case where data records are copied from memory 250 to memory 200, synchronization descriptors 215, 225, and 235 may reflect a standby, neutral, or don't care state.

Within memory 250, the data file comprises exemplary data tables 260, 270, and 280, arbitrarily labeled "Table 1," "Table 2," and "Table 3," respectively, in FIGURE 2. Memory 250 also comprises synchronization descriptor 265 (arbitrarily labeled "SD 1"), which is associated with data table 260, synchronization

descriptor 275 (arbitrarily labeled "SD 2"), which is associated with data table 270, and synchronization descriptor 285 (arbitrarily labeled "SD 3"), which is associated with data table 280. Synchronization descriptors 265, 275, and 285 of memory 250 may be in a standby, neutral, or don't care state when source data records are being copied from memory 200 to memory 250. However, in the event of a change in the direction of data record copy transfer, synchronization descriptors 265, 275, and 285 function as previously described for synchronization descriptors 215, 225, and 235 in memory 200.

10 Data table 210 comprises a plurality of data records 211 through 214 (arbitrarily labeled "Record 1" through "Record N", respectively) which store source data records. Similarly, data table 260 comprises a plurality of copy data records 261 through 264 (arbitrarily labeled "Record 1" through "Record N"), with each data record in memory 250 respectively storing a copy of the equivalent source data record in memory 210. In other words, Record 1 in data table 210 is copied over to Record 1 in data table 260, Record 2 in data table 210 is copied over to Record 2 in data table 260, and so on.

15 Bulk copy controller 290 controls the bulk copy of data records from memory 200 to memory 250, when enabled. In one

embodiment, bulk copy controller 290 may begin the bulk copy of data records in data tables 210, 225, and 235 when synchronization descriptors 215, 225, and 235 are all zero. In another embodiment, bulk copy controller 290 may begin the bulk copy of source data 5 records when bulk copy controller 290 indirectly receives a bulk copy request from update controller 295 through memory 200. After each source data record is copied to memory 250, bulk copy controller 290 may increment the status of the synchronization descriptor associated with the copied data record as an indication 10 that update controller 295 may begin to monitor the source data records for changes. Bulk copy controller 290 continues to copy source data records and to increment synchronization descriptors until all source data records have been copied from memory 200 to memory 250. When the bulk copy process is complete, 15 synchronization descriptors 215, 225, and 235 reflect their highest numbered source data record.

When data is required to be copied from memory 250 to memory 200, bulk copy controller 290 may perform the same functions as described above, although the data and synchronization 20 descriptors of memory 250 are now controlled and monitored.

Update controller 295 may initiate the bulk transfer of source data to memory 250 and may provide updates for changed source data

records after the initial copy transfer of a particular data record. As previously described, update controller 295 may set synchronization descriptors 215, 225, and 235 to zero when update controller 295 determines that memory 250 is offline or otherwise requires the bulk copy of source data records from memory 200. As long as synchronization descriptors 215, 225, and 235 are zeroes, update controller 295 provides no data record update for memory 250. As synchronization descriptors are incremented from zero to a non-zero value, update controller 295 monitors the associated source data record(s) to detect write operations that change data in source data records that are less than or equal to the value of SD1, SD2, or SD3. Update controller 295 increases the number of monitored source data records by one data record with each increment of a synchronization descriptor by bulk copy controller 290.

At any instant in time, update controller 295 may update a data record with a record number equal to or less than the value of the synchronization descriptor for that table in memory 200. Ultimately, update controller 295 monitors the status of all source data records associated with each source data table 210, 220, and 230 as reflected by the highest numbers on associated synchronization descriptors 215, 225, and 235. In the event that

data records are copied from memory 250 to memory 200, update controller 295 provides the same functions as described above for synchronization descriptors and data records located in memory 250 rather than memory 200.

5 FIGURE 3 depicts flow diagram 300, which illustrates the operation of bulk copy controller 290 and update controller 295 in exemplary switch 111 and/or exemplary base station 160 according to one embodiment of the present invention. Typically, a bulk copy process in which all data records from source memory 200 are copied to memory 250 follows a period of time in which memory 250 is not available for update. Upon detecting that the copy data file in memory 250 has gone "offline" and is available to receive a complete or bulk copy of source data files from memory 200, update controller 290 sets synchronization descriptors 215, 225, and 235 for source data tables 210, 220, and 230, respectively, to zeroes (process step 305).

10 Next, bulk copy controller 290 copies data records one at a time from first source data table 210 to first copy data table 261. After each source data record is transferred, bulk copy controller 290 increments the synchronization descriptor associated 15 with the source data record in memory 200 (process step 310). After a specific source table is completely copied and transferred,

bulk copy controller 290 begins copying and transferring data from the next data table in the source data file. Bulk copy controller 290 continues to increment the synchronization descriptor associated with each data record until each source data table in memory 200 has been copied and transferred to the appropriate data table in memory 250 (process step 315).

Operating simultaneously with bulk copy controller 290, update controller 295 monitors source synchronization descriptors in memory 200 for the presence of non-zero synchronization descriptor values (process step 320). Update controller 295 interprets the presence of a non-zero synchronization descriptor as an indication of the highest numbered source data record that may have received new source data since the indicated data record was copied and transferred to memory 250. For each non-zero synchronization descriptor, update controller 295 examines data records less than or equal to the value of the synchronization descriptor for changes with respect to the data record as stored in memory 250 (process step 325). In one embodiment of the present invention, update controller 295 detects changes in the data records of the source data file by detecting data write operations to any data record less than or equal to the value of the synchronization descriptor.

When changed data is found in a previously transferred source

data record in memory 200, update controller 295 transfers a copy of the changed data record from memory 200 to the appropriate data record location in memory 250 (process step 330). By way of example, if SD1 = 4, then update controller 295 detects write operations in Record 1 through Record 4 in data table 210 and copies the changed data to Record 1 through Record 4 in data table 250. When the bulk copy process is first initiated, update controller 295 has no data records to examine for change since new source data has not been transferred to copy memory 250, as indicated by the zero value in all synchronization descriptors in memory 200. However, the number of data records examined by update controller 295 increases with each data record copied and transferred by bulk controller 290.

Once the bulk copy and transfer of all source data records is complete, bulk controller 290 examines memory 200 for all zero values in the synchronization descriptors in memory 200. Simultaneously, update controller 295 continuously examines data records in memory 200 for changes and copies changed data records to memory 200, while also determining if the updating process needs to stop (other side offline) and then detecting when a bulk copy process can be performed (other side back online).

Although the present invention has been described in detail,

those skilled in the art should understand that they can make various changes, substitutions and alterations herein without departing from the spirit and scope of the invention in its broadest form.

WHAT IS CLAIMED IS:

1 1. A data synchronization apparatus for maintaining
2 synchronization between a source data file and a copy data file
3 comprising:

4 a bulk copy controller capable of copying a plurality of
5 data records from said source data file to said copy data file; and

6 an update controller capable of detecting a change in a
7 data record previously copied by said bulk copy controller from
8 said source data file to said copy data file and copying said
9 changed data record from said source data file to said copy data
10 file.

1 2. The data synchronization apparatus set forth in Claim 1
2 wherein said update controller and said bulk copy controller
3 operate substantially concurrently.

1 3. The data synchronization apparatus set forth in Claim 1
2 wherein said source data file comprises at least one data table
3 comprising a plurality of data records and a synchronization
4 descriptor associated with said at least one data table.

1 4. The data synchronization apparatus set forth in Claim 3
2 wherein said bulk copy controller sequentially copies said
3 plurality of data records in said at least one data table in said
4 source data file to said copy data file and sets said
5 synchronization descriptor to an index value of a most recently
6 copied one of said plurality of data records.

1 5. The data synchronization apparatus set forth in Claim 4
2 wherein said update controller detects changes in said plurality of
3 data records in said at least one data table in said source data
4 file by monitoring selected ones of said plurality of data records
5 in said at least one data table in said source data file having an
6 index value less than said index value in said synchronization
7 descriptor.

1 6. The data synchronization apparatus set forth in Claim 5
2 wherein said update controller detects said changes in said
3 plurality of data records in said at least one data table in said
4 source data file by monitoring data write operations in said
5 plurality of data records in said at least one data table in said
6 source data file.

1 7. The data synchronization apparatus set forth in Claim 6
2 wherein said update controller is capable of detecting that said
3 copy data file is off line and has lost synchronization with said
4 source data file.

1 8. The data synchronization apparatus set forth in Claim 7
2 wherein said update controller is capable of determining that said
3 copy data file is on line and is capable of activating said bulk
4 copy controller by setting at least one synchronization descriptor
5 in said source data file to a zero value.

1 9. A telecommunication device comprising:

2 a primary processing system comprising a first memory
3 capable of storing a source data file;

4 a secondary processing system comprising a second memory
5 capable of storing a copy data file; and

6 a data synchronization apparatus coupled to said first
7 and second memories for maintaining synchronization between said
8 source data file and said copy data file, said data synchronization
9 apparatus comprising:

10 a bulk copy controller capable of copying a
11 plurality of data records from said source data file to said
12 copy data file; and

13 an update controller capable of detecting a change
14 in a data record previously copied by said bulk copy
15 controller from said source data file to said copy data file
16 and copying said changed data record from said source data
17 file to said copy data file:

1 10. The telecommunications device set forth in Claim 9
2 wherein said update controller and said bulk copy controller
3 operate substantially concurrently.

1 11. The telecommunications device set forth in Claim 9
2 wherein said source data file comprises at least one data table
3 comprising a plurality of data records and a synchronization
4 descriptor associated with said at least one data table.

1 12. The telecommunications device set forth in Claim 11
2 wherein said bulk copy controller sequentially copies said
3 plurality of data records in said at least one data table in said
4 source data file to said copy data file and sets said
5 synchronization descriptor to an index value of a most recently
6 copied one of said plurality of data records.

1 13. The telecommunications device set forth in Claim 12
2 wherein said update controller detects changes in said plurality of
3 data records in said at least one data table in said source data
4 file by monitoring selected ones of said plurality of data records
5 in said at least one data table in said source data file having an
6 index value less than said index value in said synchronization
7 descriptor.

1 14. The telecommunications device apparatus set forth in
2 Claim 13 wherein said update controller detects said changes in
3 said plurality of data records in said at least one data table in
4 said source data file by monitoring data write operations in said
5 plurality of data records in said at least one data table in said
6 source data file.

1 15. The telecommunications device set forth in Claim 14
2 wherein said update controller is capable of detecting that said
3 copy data file is off line and has lost synchronization with said
4 source data file.

1 16. The telecommunications device set forth in Claim 15
2 wherein said update controller is capable of determining that said
3 copy data file is on line and is capable of activating said bulk
4 copy controller by setting at least one synchronization descriptor
5 in said source data file to a zero value.

1 17. A method of maintaining synchronization between a source
2 data file and a copy data file comprising:

3 sequentially copying a plurality of data records from the
4 source data file to the copy data file; and

5 detecting a change in a data record previously copied in
6 the step of sequentially copying and copying the changed data
7 record from the source data file to the copy data file.

1 18. The method as set forth in Claim 17 wherein the step of
2 sequentially copying and the step of detecting a change are
3 performed substantially concurrently.

1 19. The method as set forth in Claim 17 wherein the source
2 data file comprises at least one data table comprising a plurality
3 of data records and a synchronization descriptor associated with
4 the at least one data table.

1 20. The method as set forth in Claim 19 wherein the step of
2 sequentially copying comprises the substeps of:

3 sequentially copying the plurality of data records in the
4 at least one data table in the source data file to the copy data
5 file; and

6 setting the synchronization descriptor to an index value
7 of a most recently copied one of the plurality of data records.

1 21. The method as set forth in Claim 20 wherein the step of
2 detecting a change comprises the substep of monitoring selected
3 ones of the plurality of data records in the at least one data
4 table in the source data file having an index value less than the
5 index value in the synchronization descriptor.

DATA SYNCHRONIZATION SYSTEM AND METHOD OF OPERATION**ABSTRACT OF THE DISCLOSURE**

There is disclosed a data synchronization apparatus for maintaining synchronization between a source data file and a copy data file. The data synchronization apparatus comprises: 1) a bulk copy controller for copying a plurality of data records from the source data file to the copy data file; and 2) an update controller for detecting a change in a data record previously copied by the bulk copy controller from the source data file to the copy data file and copying the changed data record from the source data file to the copy data file.

SAMS01-00077
SHEET 1 OF 3

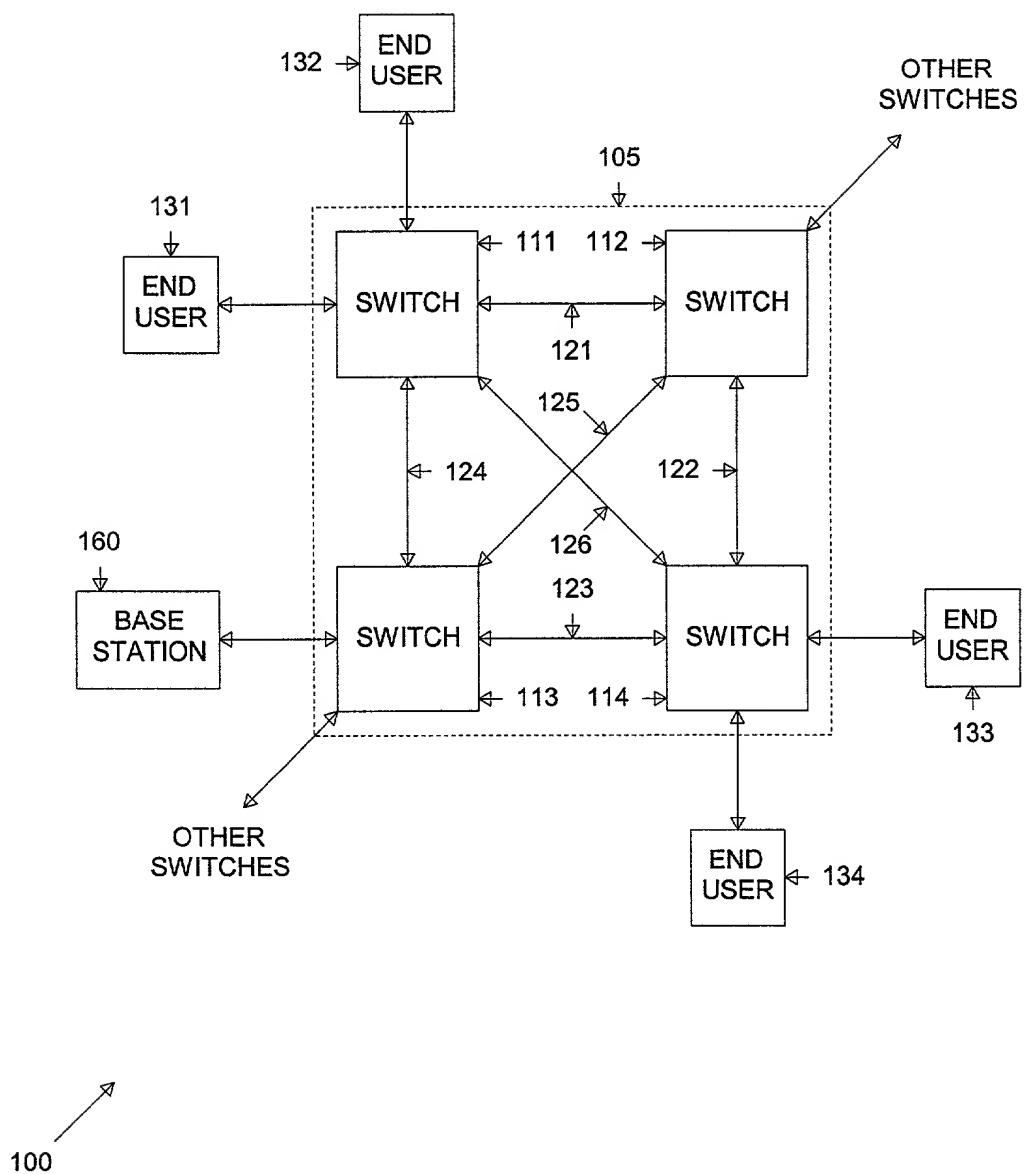


FIGURE 1

SAMS01-00077
SHEET 2 OF 3

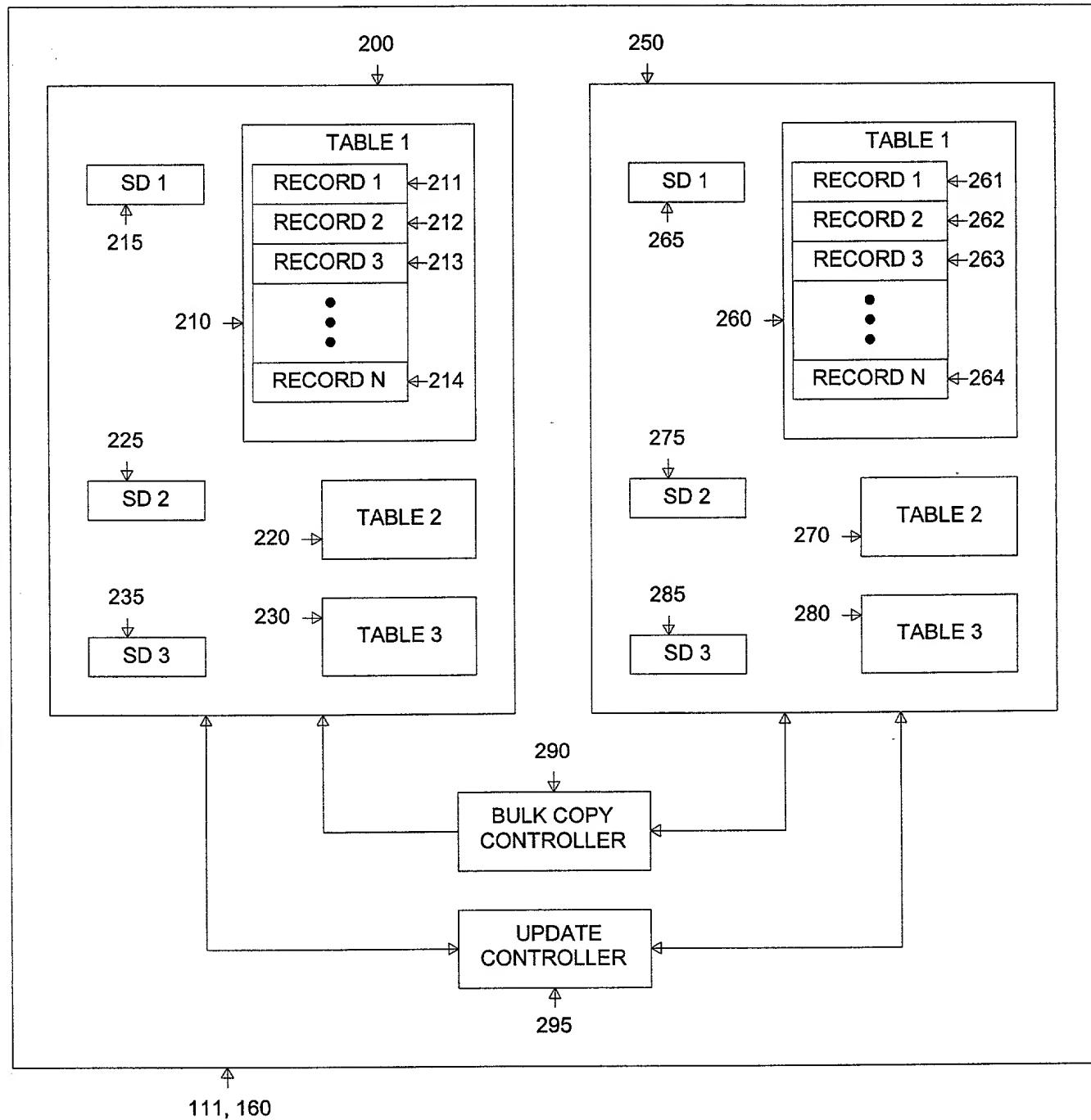


FIGURE 2

SAMS01-00077
SHEET 3 OF 3

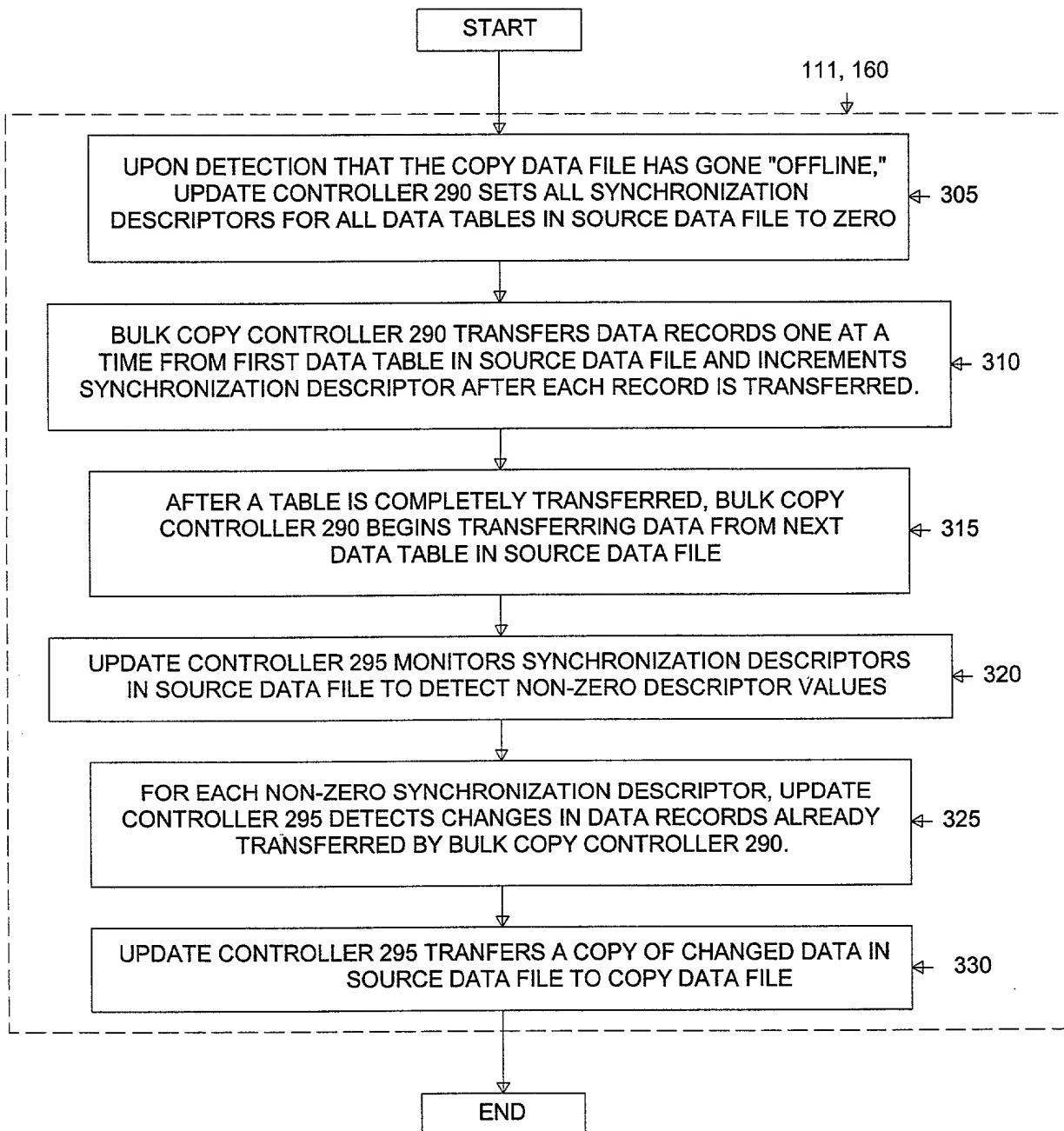


FIGURE 3

DECLARATION AND POWER OF ATTORNEY

As a below named sole inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am an original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention, design or discovery entitled:

"DATA SYNCHRONIZATION SYSTEM AND METHOD OF OPERATION"

the specification of which is attached hereto.

I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above;

I acknowledge the duty to disclose to the Office all information known to me to be material to the patentability of this application as defined by Title 37, Code of Federal Regulations, § 1.56.

I hereby claim no foreign priority benefits under 35 U.S.C. § 119 of any foreign application(s) for patent or inventor's certificate on which priority is claimed.

I hereby claim no benefit under Title 35, United States Code, § 120 of any United States application. I acknowledge the duty to disclose to the Office all information known to me to be material to patentability as defined in § 1.56 which became available between the filing date of any prior application(s) and the national or PCT international filing date of this application.

I hereby appoint:

John C. Han, Registration No. 41,403

of SAMSUNG TELECOMMUNICATIONS AMERICA, INC., and

William A. Munck, Registration No. 39,308
John T. Mockler, Registration No. 39,775

all of the firm of NOVAKOV ♦ DAVIS, P.C. my attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith, and to file and prosecute any international patent applications filed thereon before any international authorities under the Patent Cooperation Treaty.

Send correspondence to: William A. Munck, Esq.
NOVAKOV ♦ DAVIS, P.C.
2000 St. Paul Place
750 North St. Paul Street
Dallas, Texas 75201-3286

Direct telephone calls to: William A. Munck, Esq.
(214) 922-9221

Atty. Docket No.: SAMS01-00077

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under § 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Full name of inventor: Sandro Gregorat

Inventor's signature: 

Date: 12-6-99

Residence (City, County, State): Dallas, Collin County, Texas

Citizenship: United States of America

Post Office Address: 17512 Spyglass Circle
Dallas, Texas 75287